AN: PAT 1997-552835

TI: Electronic module with redundant architecture for fault tolerant operation has tasks brought into synchronism in redundant modules and compares address, data and control buses to detect discrepancy between two processors

PN: **FR2748136**-A1

PD: 31.10.1997

AB: The electronic module for a computer rack has two processors (MP1,MP2) working in redundant but non-synchronous mode. Control circuits (CS1,CS2) synchronise the outcome of certain tasks are in agreement. The tasks are brought into synchronism by delaying the result of the first task to finish. The control circuits detect commencement of execution of the specified tasks, and initiate a timer when the task has been started by one of the processors. The task is blocked on the first processor until it is started by the second processor. The address, data and control buses of the processors are compared to determine whether there has been an error.; USE - For avionic systems in aircraft. ADVANTAGE - High rate of error detection with reduced level of false alarms in high integrity system using modular redundancy.

PA: (SEXT-) SEXTANT AVIONIQUE;

IN: DUCATEAU M;

FA: **FR2748136**-A1 31.10.1997;

CO: FR;

IC: G06F-011/16;

MC: T01-G03; T01-G05B; U21-A06;

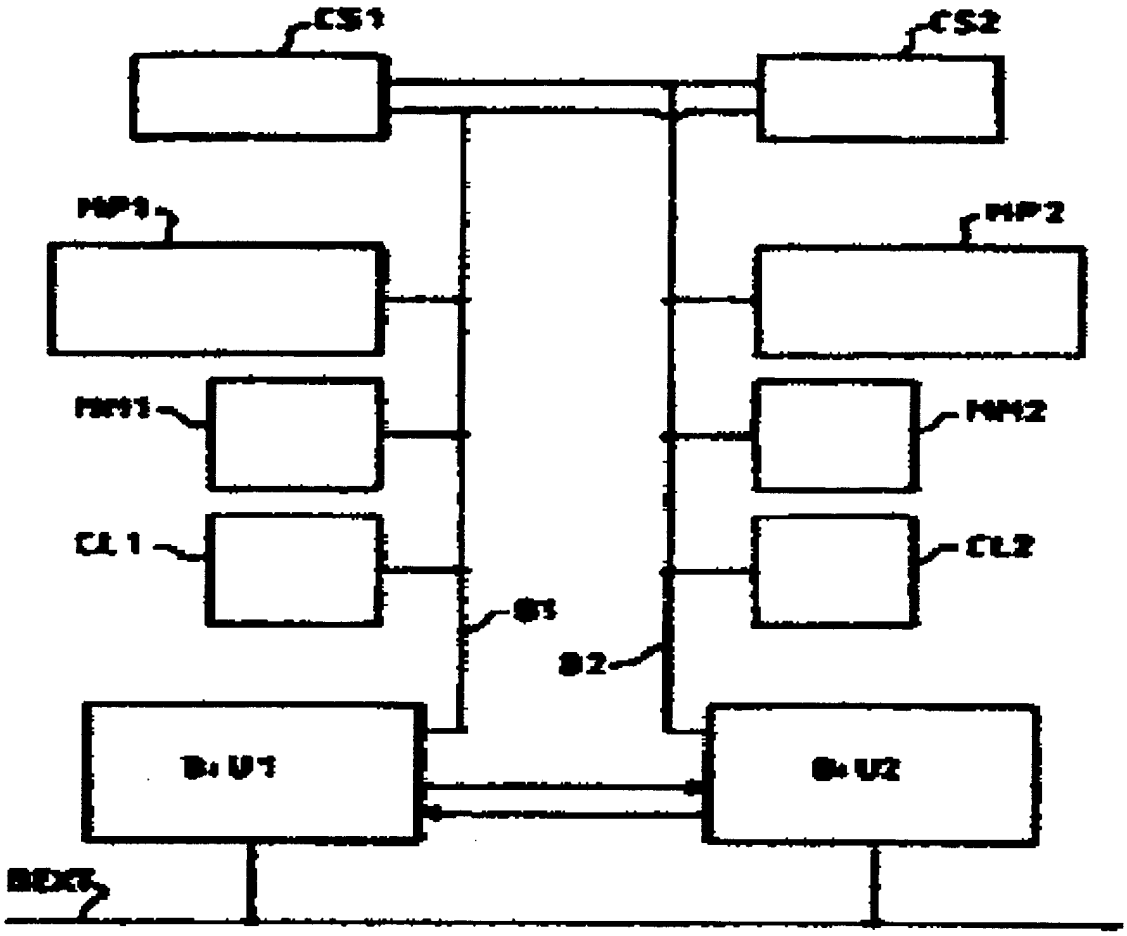
DC: T01; U21;

FN: 1997552835.gif

PR: FR0005424 30.04.1996;

FP: 31.10.1997

UP: 15.12.1997


CS1
CS2
PMP1
PMP2
PMM1
PMM2
CL1
CL2
B1
B2
B+U1
B+U2